



CLOVER DISPLAY LTD.

LCD MODULE SPECIFICATION

Model: CG160160D - _ _ - _ _ - _ _ - _ _

Revision	02
Engineering	Kemp Huang
Date	19 December 2013
Our Reference	X9043

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MODE OF DISPLAY**Display mode**

- STN : Yellow green
 Grey
 Blue (negative)
 FSTN positive
 FSTN negative

Display condition

- Reflective type
 Transflective type
 Transmissive type
 Others

Viewing direction

- 6 O' clock
 12 O' clock
 3 O' clock
 9 O' clock

LCD MODULE NUMBER NOTATION:

CG160160D- N N - S R - N 6 - T

| | | | | | | |
(1) (2) (3) (4) (5) (6) (7) (8)

*(1)---Model number of standard LCD Modules

*(2)---Backlight type

- N – No backlight
E – EL backlight
L – Side-lited LED backlight
M– Array LED backlight
C – CCFL

*(3)---Backlight color

- N – No backlight
A – Amber
B – Blue
O– Orange
W–White
Y – Yellow green

*(4)---Display mode

- T – TN
V – TN (Negative)
S – STN Yellow green
G – STN Grey
B – STN Blue (Negative)
F – FSTN
N – FSTN (Negative)

*(5)---Rear polarizer type

- R – Reflective
F – Transflective
T – Transmissive

*(6)---Temperature range

- N – Normal
W– Extended

*(7)---Viewing direction

- 6 – 6 O'clock
2 – 12 O'clock
3 – 3 O'clock
9 – 9 O'clock

*(8)---Special code for other requirements
(Can be omitted if not used)

GENERAL DESCRIPTION

Display mode	:	160 X 160 dots, graphic COG LCD module
Interface	:	4-bit or 8-bit parallel/serial/I ² C
Driving method	:	1/160 duty, 1/11 bias
Controller IC	:	Ultrachip UC1611S or equivalent For the detailed information, please refer to the IC specifications.

MECHANICAL DIMENSIONS

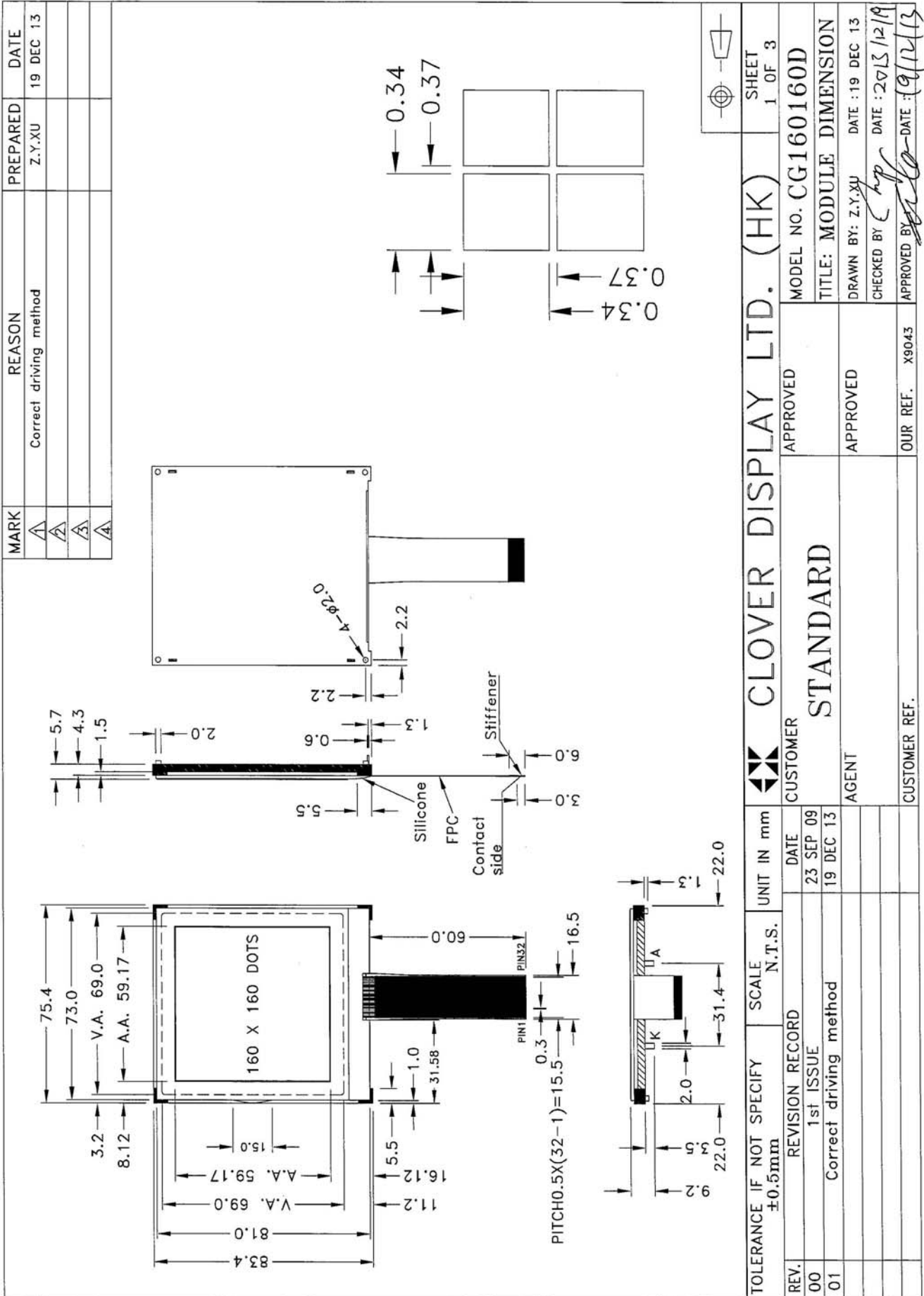
Item		Dimension	Unit	Item	Dimension	Unit
Outline Dimension	No backlight	74.0(L)x81.0(W)x2.9(H)	mm	Dot Size	0.34(L)x0.34(W)	mm
	LEDside-lited backlight	75.4(L)x83.4(W)x9.2(H)	mm	Dot Pitch	0.37(L)x0.37(W)	mm
Active Area		59.17(L)x59.17(W)	mm	Viewing Area	69.0(L)x69.0(W)	mm

CONNECTOR PIN ASSIGNMENT

Pin No.	Symbol	Function	Pin No.	Symbol	Function
1	VB0+	LCD bias voltages	18	WR0	Read/write operation control
2	VB1+		19	CD	Register select
3	VB1-		20	CS1	Chip select
4	VB0-		21	CS0	
5	VA0+		22	RST	Reset
6	VA1+		23	D0	Data bus
7	VA1-		24	D1	
8	VA0-		25	D2	
9	VLCD	LCD Power supply	26	D3	
10	VDD	Power supply for logic	27	D4	
11	VDD		28	D5	
12	VSS	Power supply for logic (Ground)	29	D6	
13	VSS		30	D7	
14	ID0	Production control	31	D13	Mode select
15	BM0	Mode select	32	D15	
16	BM1		*33	A	Supply voltage for backlight (+VE)
17	WR1	Read/write operation control	*34	K	Supply voltage for backlight (-VE)

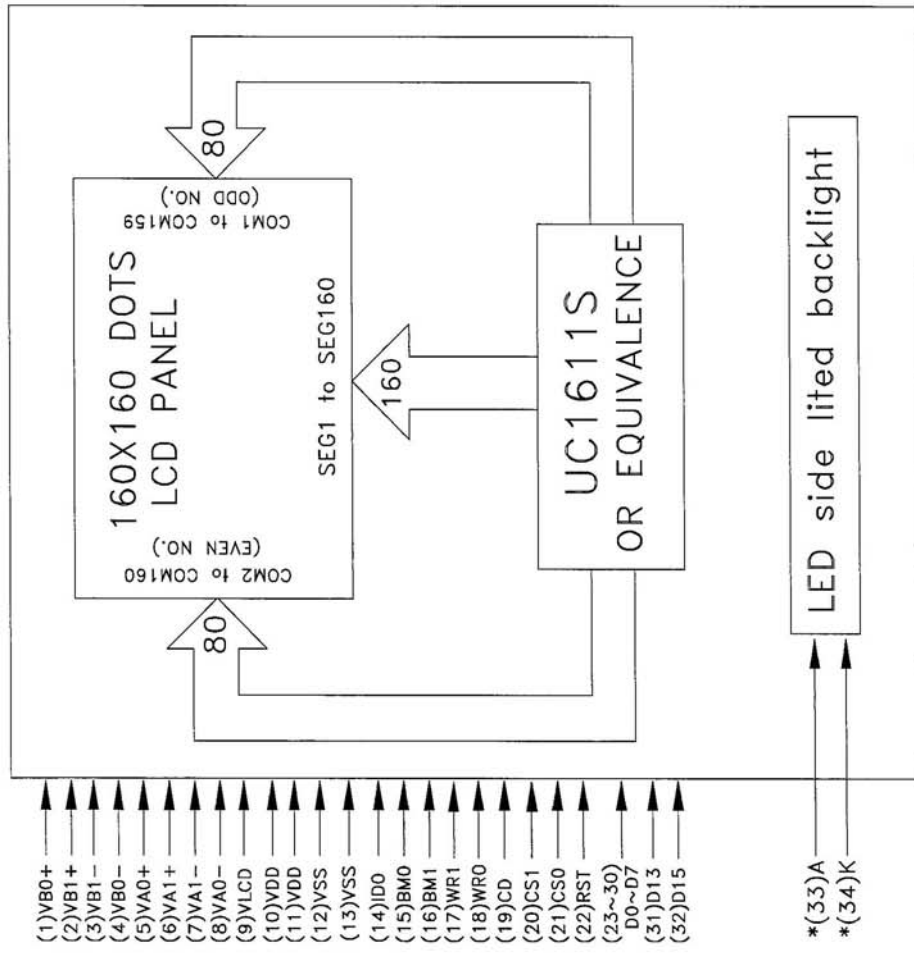
Note (*): Pin 33, 34 are used for backlight version

COUNTER DRAWING OF MODULE DIMENSION



COUNTER DRAWING OF PIN OUT & BLOCK DIAGRAM

PIN NO.	SYMBOL	FUNCTION
1	VB0+	LCD bias voltages
2	VB1+	
3	VB1-	
4	VB0-	
5	VA0+	
6	VA1+	
7	VA1-	
8	VA0-	
9	VLCD	LCD power supply
10	VDD	Power supply for logic(VDD)
11	VDD	
12	VSS	Power supply for logic(Ground)
13	VSS	
14	ID0	Production control
15	BM0	Mode select
16	BM1	
17	WR1	Read/write operation control
18	WRO	
19	CD	Register select
20	CS1	Chip select
21	CS0	
22	RST	Reset
23	D0	Data bus
24	D1	
25	D2	
26	D3	
27	D4	
28	D5	
29	D6	
30	D7	
31	D13	Mode select
32	D15	
*33	A	Supply voltage for backlight(+VE)
*34	K	Supply voltage for backlight(-VE)



NOTE: * for the module with backlight version

TOLERANCE IF NOT SPECIFY ±0.5mm		SCALE N.T.S.	UNIT IN mm	CLOVER DISPLAY LTD. (HK)		SHEET 2 OF 3
REV.	REVISION RECORD	DATE	CUSTOMER	APPROVED	MODEL NO. CG160160D	TITLE: PIN OUT & BLOCK DIAGRAM
00	1st ISSUE	23 SEP 09	STANDARD	APPROVED	DRAWN BY: Z.Y.XU	DATE : 19 DEC 13
01	Correct driving method	19 DEC 13	AGENT	APPROVED	CHECKED BY: <i>Chp</i>	DATE : 2013/12/17
			CUSTOMER REF.	OUR REF. X9043	APPROVED BY: <i>John</i>	DATE: 19/12/13

ELECTRICAL CHARACTERISTICS

Conditions: VSS=0V, Ta=25°C

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage for Logic	VDD	3.05	3.3	3.55	V
Supply Current for Logic	IDD	—	1.4	2.1	mA
Operating voltage for LCD (*)	VLCD	13.8	14.5	15.2	V
“H”Level Input Voltage	VIH	0.8VDD	—	—	V
“L”Level Input Voltage	VIL	—	—	0.2VDD	V

Note (*): There is tolerance in optimum LCD driving voltage during production and it will be within the specified range.

Side Backlight:

Constant voltage driving:

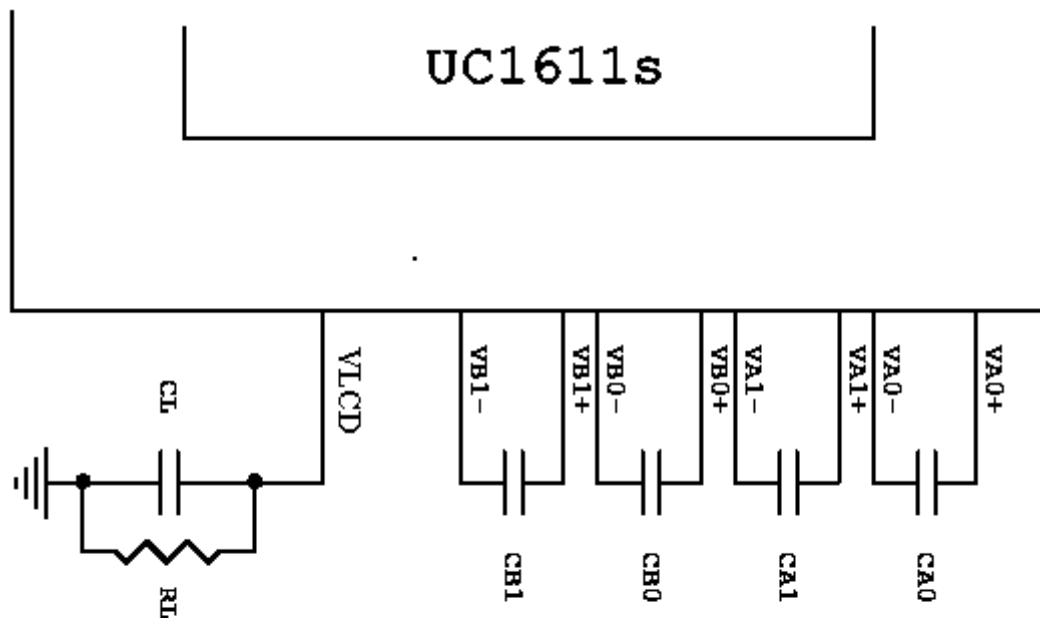
Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
White Backlight Current	IBL	—	60	80	mA	VBL = 3.3V
Blue Backlight Current	IBL	—	60	80	mA	VBL = 3.3V

ABSOLUTE MAXIMUM RATINGS

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	VDD	-0.3 to +4.0	-0.3 to +4.0	V
Input Voltage	VT	-0.4 to VDD +0.5	-0.4 to VDD +0.5	V
Operating Temperature	Topr	0 to 50	-20 to 70	°C
Storage Temperature	Tstg	-10 to 60	-30 to 80	°C

REFERENCE CIRCUIT EXAMPLE



CA0,CA1,CB0,CB1=4.7uF

RL=5M,CL=0.1uF

INSTRUCTIONS TABLE

(Note) *: disabled data

The following list of host commands is supported by UC1611s

C/D: 0: Control 1: Data

W/R: 0: Write cycle 1: Read cycle

Effective Data bits

- Don't Care

	Command	C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action	Default	
1.	Write Data Byte	1	0	#	#	#	#	#	#	#	#	Write 1 byte	N/A	
2.	Read Data Byte	1	1	#	#	#	#	#	#	#	#	Read 1 byte	N/A	
3.	Get Status	0	1	Ver	MX	MY	WA	DE	WS	MD	MS	Get Status	N/A	
				ID[1:0]			PMO[5:0]							
				Product Code				0	0	0	EF			
4.	Set Column Addr. LSB	0	0	0	0	0	0	#	#	#	#	Set CA[3:0]	0	
	Set Column Addr. MSB	0	0	0	0	0	1	#	#	#	#	Set CA[7:4]	0	
5.	Temp. Compensation.	0	0	0	0	1	0	0	1	#	#	Set TC[1:0]	00b: -0.05%/°C	
6.	Set Panel Loading	0	0	0	0	1	0	1	0	#	#	Set PC [1:0]	11b: 33~55 nF	
7.	Set Pump Control	0	0	0	0	1	0	1	1	#	#	Set PC [3:2]	11b	
8.	Set Adv. Program Control (double-byte command)	0	0	0	0	1	1	0	0	R	R	Set APC[R][7:0] R = 0~3	N/A	
				#	#	#	#	#	#	#	#			
9.	Set Scroll Line LSB	0	0	0	1	0	0	#	#	#	#	Set SL[3:0]	0	
	Set Scroll Line MSB			0	1	0	1	#	#	#	#	Set SL[7:4]	0	
10.	Set Page Address LSB	0	0	0	1	1	0	#	#	#	#	Set PA[3:0]	0	
	Set Page Address MSB			0	1	1	1	0	#	#	#	Set PA[6:4]	0	
11.	Set Potentiometer (double-byte command)	0	0	1	0	0	0	0	0	0	1	Set PM[7:0]	PM=EAH	
				#	#	#	#	#	#	#	#			
12.	Set Isolation Clock Front	0	0	1	0	0	0	0	0	1	0	Set ISOF[3:0]	1H	
				0	0	0	1	0	0	1	1			
13.	Set Isolation Clock Back	0	0	-	-	-	-	#	#	#	#	Set ISOB[3:0]	0H	
				1	0	0	0	0	0	1	0			
14.	Set Partial Display Control	0	0	1	0	0	0	0	1	#	#	Set LC[9:8]	00b: Disable	
15.	Set RAM Address Control	0	0	1	0	0	0	1	#	#	#	Set AC[2:0]	001b	
16.	Set Fixed Lines	0	0	1	0	0	1	#	#	#	#	Set FL[3:0]	0	
17.	Set Line Rate	0	0	1	0	1	0	0	0	#	#	Set LC[5:4]	10b:28klps	
18.	Set All-Pixel-ON	0	0	1	0	1	0	0	1	0	#	Set DC[1]	0	
19.	Set Inverse Display	0	0	1	0	1	0	0	1	1	#	Set DC[0]	0	
20.	Set Display Enable	0	0	1	0	1	0	1	#	#	#	Set DC[4:2]	110b	
21.	Set LCD Mapping Control (double-byte command)	0	0	1	1	0	0	0	0	0	0	Set LC[3:0]	0	
				0	0	0	0	#	#	#	#			
22.	Set N-line Inversion (double-byte command)	0	0	1	1	0	0	1	0	0	0	Set NIV[6:0]	00H	
				-	#	#	#	#	#	#	#			
23.	Set Display Pattern	0	0	1	1	0	1	0	#	#	#	Set DC[7:5]	000b	
24.	System Reset	0	0	1	1	1	0	0	0	1	0	System Reset	N/A	
25.	NOP	0	0	1	1	1	0	0	0	1	1	No operation	N/A	
26.	Set test control (double-byte command)	0	0	1	1	1	0	0	1	TT		For testing only. Do not use.	N/A	
		0	0	#	#	#	#	#	#	#	#			
27.	Set LCD Bias Ratio	0	0	1	1	1	0	1	0	#	#	Set BR[1:0]	10b: 11	
28.	Set COM End	0	0	1	1	1	1	0	0	0	1	Set CEN[7:0]	159	
		0	0	#	#	#	#	#	#	#	#			
29.	Set Partial Display Start	0	0	1	1	1	1	0	0	1	0	Set DST[7:0]	0	
		0	0	#	#	#	#	#	#	#	#			
30.	Set Partial Display End	0	0	1	1	1	1	0	0	1	1	Set DEN[7:0]	159	
		0	0	#	#	#	#	#	#	#	#			

Command		C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action	Default	
31.	Set Window Program Starting Column Address	0	0	1	1	1	1	0	1	0	0	Shared with MTP Commands	Set WPC0	0
32.	Set Window Program Starting Row Address	0	0	1	1	1	1	0	1	0	1		Set WPP0	0
33.	Set Window Program Ending Column Address	0	0	1	1	1	1	0	1	1	0		Set WPC1	255
34.	Set Window Program Ending Column Address	0	0	1	1	1	1	0	1	1	1		Set WPP1	79
35.	Window Program Mode	0	0	1	1	1	1	1	0	0	#	Set AC[3]	0:Inside	
36.	Set MTP Operation Control	0	0	1	0	1	1	1	0	0	0	Set MTPC[5:0]	10H	
37.	Set MTP Write Mask	0	0	1	0	1	1	1	0	0	1	Set MTPM[5:0]	0	
38.	Set V _{MTP1} Potentiometer	0	0	1	1	1	1	0	1	0	0	Shared with Window Program Commands	Set MTP1	N/A
39.	Set V _{MTP2} Potentiometer	0	0	1	1	1	1	0	1	0	1		Set MTP2	N/A
40.	Set MTP Write Timer	0	0	1	1	1	1	0	1	1	0		Set MTP3	N/A
41.	Set MTP Read Timer	0	0	1	1	1	1	0	1	1	1		Set MTP4	N/A
SERIAL READ COMMAND (ENABLE IN S8 OR S9 BUS MODES ONLY)														
42.	Get Status	0	0	1	1	1	1	1	1	1	0	Get Status till Chip Disable	N/A	
		-	1	Ver	MX	MY	WA	DE	WS	MD	MS			
				ID[1:0]	PMO[5:0]				Product Code	0	0			0

Notes:

- All bit patterns other than commands listed above may result in undefined behavior.
- Commands (38)~(41) are shared with commands (31)~(34), and have exactly the same code. When MTPC[3]=0, commands (37)~(41) are interpreted as Window Programming commands. When MTPC[3]=1, they are MTP Control commands.
- MTPM and PM are actually the same register. Only one of the commands (36) is valid at any time, and it is determined by MTPC[3].
- After MTP-ERASE or MTP-PROGRAM operation, please always perform the following steps,
 - a) Disconnect TST4 power source.
 - b) Do a full V_{DD} ON-OFF cycle (make sure V_{DD} drops below 50mV).
 before resuming normal operation.

RECOMMENDED INITIAL SETTINGS

System Reset: E2H

Set Temp. Compensation: 24H

Set up LCD format specific parameters MX,MY,etc(double-byte command): C0H,04H

Set line rate: A3H

Set Pump Control (internal Vlcd): 2FH

Set Isolation Clock Front (3 bytes command): 82H, 13H, 01H

Set Isolation Clock Back (3 bytes command): 82H, 14H, 00H

Set LCD Bias Ratio: EAH

LCD Specific Operation Voltage Setting (double-byte command): 81H, 90H

Set RAM Address Control: 80H

Set Page Addr. MSB: 72H

Set Page Addr. LSB : 60H

Set Column Addr. LSB: 00H

Set Column Addr.MSB: 10H

Window Program Enable : F8H

Window Starting Column (double-byte command): F4H , 00H

Window Ending Column (double-byte command): F6H, 9FH

Set one bit for one pixel: D1H

Set Display Enable: A9H

DISPLAY DATA RAM

MSF			RAM																MY=0				MY=1															
0	1	Line Addresss																	SL=0	SL=16	SL=0	SL=16																
D0	D4	00H	Page 0																COM1	COM145	COM160	COM16																
D1	D5	01H																	Page 1				COM2	COM146	COM159	COM15												
D2	D6	02H																					COM3	COM147	COM158	COM14												
D3	D7	03H																	Page 2				COM4	COM148	COM157	COM13												
D4	D0	04H																					COM5	COM149	COM156	COM12												
D5	D1	05H																	Page 3				COM6	COM150	COM155	COM11												
D6	D2	06H																					COM7	COM151	COM154	COM10												
D7	D3	07H																	Page 18																COM8	COM152	COM153	COM9
D0	D4	08H	Page 19				COM9	COM153	COM152	COM8																												
D1	D5	09H					COM10	COM154	COM151	COM7																												
D2	D6	0AH	Page 19				COM11	COM155	COM150	COM6																												
D3	D7	0BH					COM12	COM156	COM149	COM5																												
D4	D0	0CH	Page 19																																COM13	COM157	COM148	COM4
D5	D1	0DH																																	COM14	COM158	COM147	COM3
D6	D2	0EH																																	COM15	COM159	COM146	COM2
D7	D3	0FH																	COM16	COM160	COM145	COM1																
D0	D4	10H																	Page 19																COM17	COM1	COM144	COM160
D1	D5	11H																																	COM18	COM2	COM143	COM159
D2	D6	12H																																	COM19	COM3	COM142	COM158
D3	D7	13H																																	COM20	COM4	COM141	COM157
D4	D0	14H	COM21	COM5	COM140	COM156																																
D5	D1	15H	COM22	COM6	COM139	COM155																																
D6	D2	16H	COM23	COM7	COM138	COM154																																
D7	D3	17H	COM24	COM8	COM137	COM153																																
D0	D4	18H	Page 19																COM25	COM9	COM136	COM152																
D1	D5	19H																	COM26	COM10	COM135	COM151																
D2	D6	1AH																	COM27	COM11	COM134	COM150																
D3	D7	1BH																	COM28	COM12	COM133	COM149																
D4	D0	1CH																	COM29	COM13	COM132	COM148																
D5	D1	1DH																	COM30	COM14	COM131	COM147																
D6	D2	1EH																	COM31	COM15	COM130	COM146																
D7	D3	1FH																	COM32	COM16	COM129	COM145																
D0	D4	90H	Page 19																COM145	COM129	COM16	COM32																
D1	D5	91H																	COM146	COM130	COM15	COM31																
D2	D6	92H																	COM147	COM131	COM14	COM30																
D3	D7	93H																	COM148	COM132	COM13	COM29																
D4	D0	94H																	COM149	COM133	COM12	COM28																
D5	D1	95H																	COM150	COM134	COM11	COM27																
D6	D2	96H																	COM151	COM135	COM10	COM26																
D7	D3	97H																	COM152	COM136	COM9	COM25																
D0	D4	98H	Page 19																COM153	COM137	COM8	COM24																
D1	D5	99H																	COM154	COM138	COM7	COM23																
D2	D6	9AH																	COM155	COM139	COM6	COM22																
D3	D7	9BH																	COM156	COM140	COM5	COM21																
D4	D0	9CH																	COM157	COM141	COM4	COM20																
D5	D1	9DH																	COM158	COM142	COM3	COM19																
D6	D2	9EH																	COM159	COM143	COM2	COM18																
D7	D3	9FH																	COM160	COM144	COM1	COM17																

MX	0								1							
	SEG240	SEG239	SEG238	SEG237	SEG236	SEG235	SEG234	SEG233	SEG236	SEG237	SEG238	SEG239	SEG240			
	SEG240	SEG239	SEG238	SEG237	SEG236	SEG235	SEG234	SEG233	SEG236	SEG237	SEG238	SEG239	SEG240			

When DC[5:3]=100b :

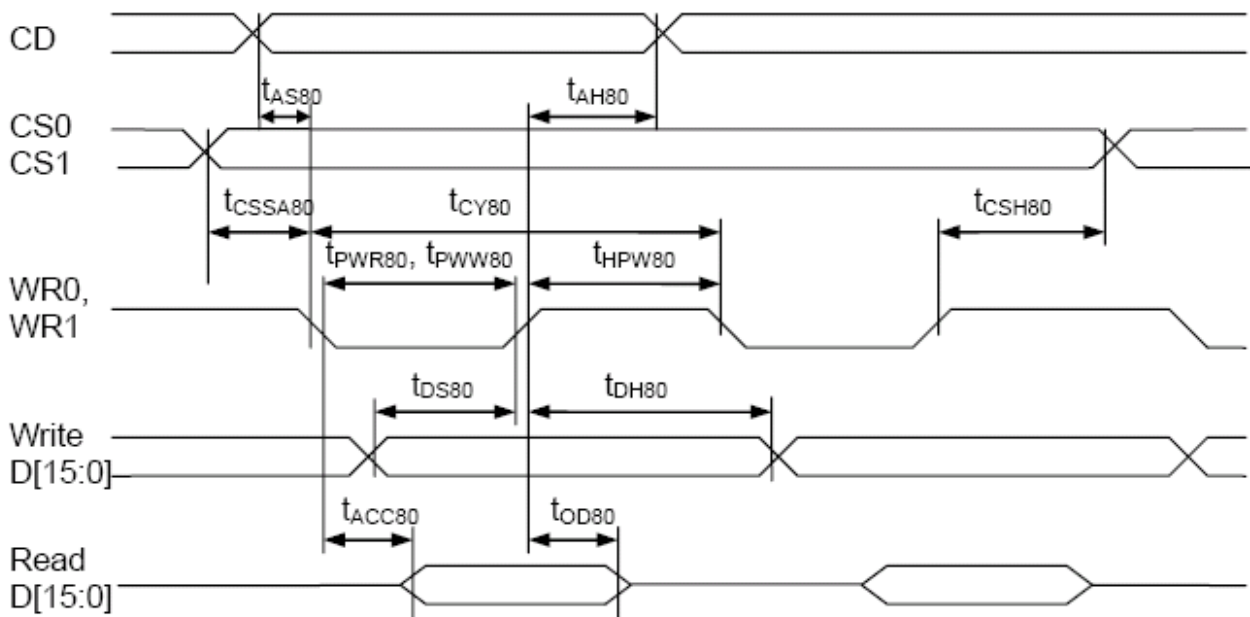
Example for memory mapping:

Let MX = 0, MY = 0, SL = 0, MSF = 0, according to the data shown in the above table:

⇒ Page 0 SEG 1 : (D[7:0]) 1000 1111 b

⇒ Page 0 SEG 2 : (D[7:0]) 0100 1100 b

PARALLEL INTERFACE TIMING DIAGRAM (8080 MODE)

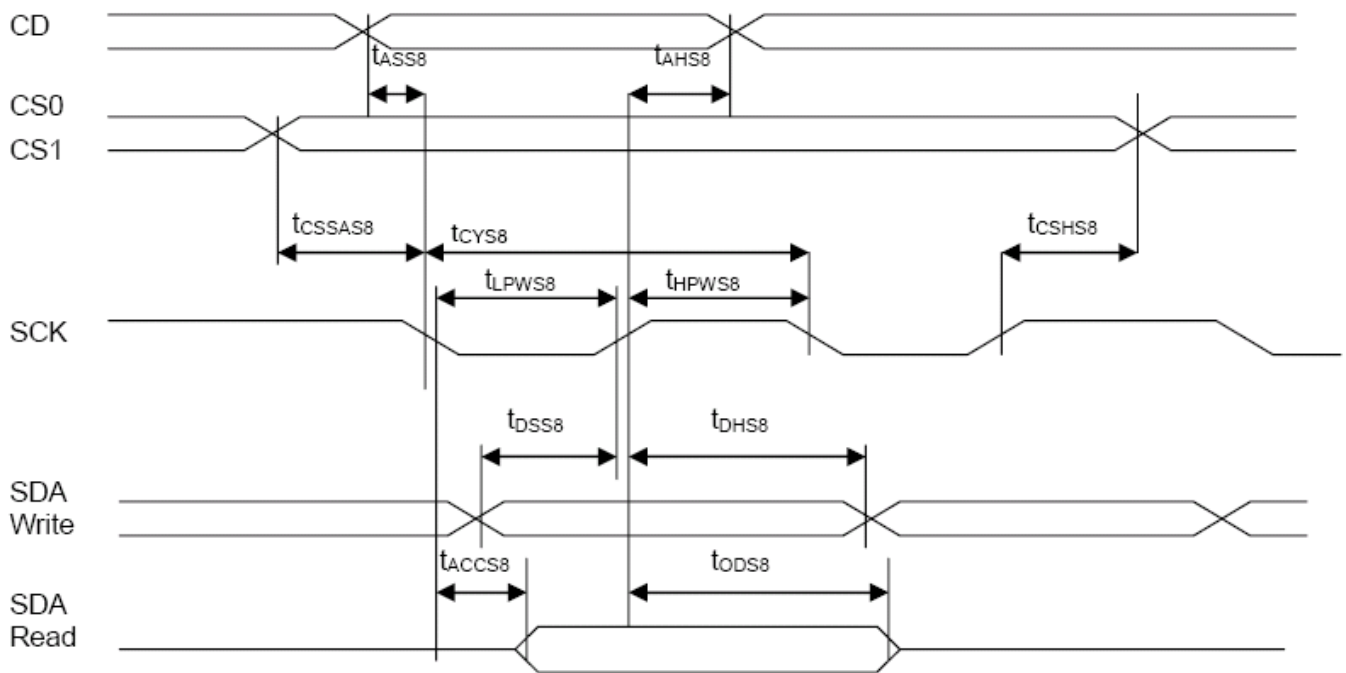


PARALLEL INTERFACE TIMING CHARACTERISTICS (8080 MODE)

($2.5V \leq V_{DD} < 3.6V$, $T_a = -30$ to $+85^\circ C$)

Symbol	Signal	Description	Condition	Min. (nS)	Max.(nS)
t_{AS80} t_{AH80}	CD	Address setup time Address hold time		0 0	–
t_{CY80}		System cycle time 16-bit bus 8-bit bus 4-bit bus	(Read / Write)	410 / 330 150 / 130 100 / 70	–
t_{PWR80} t_{PWW80}	WR1, WR0	Low Pulse width 16-bit bus 8-bit bus 4-bit bus	(Read / Write)	205 / 165 75 / 65 50 / 35	–
t_{HPW80}	WR1, WR0	High pulse width 16-bit bus 8-bit bus 4-bit bus	(Read / Write)	205 / 165 75 / 65 50 / 35	–
t_{DS80} t_{DH80}	D15~D0	Data setup time Data hold time		30 0	–
t_{ACC80} t_{OD80}		Read access time Output disable time	$C_L = 100pF$	– 30	60 –
t_{SSA80} t_{CSH80}	CS1/CS0	Chip select setup time		0 0	

SERIAL INTERFACE TIMING DIAGRAM (FOR S8)

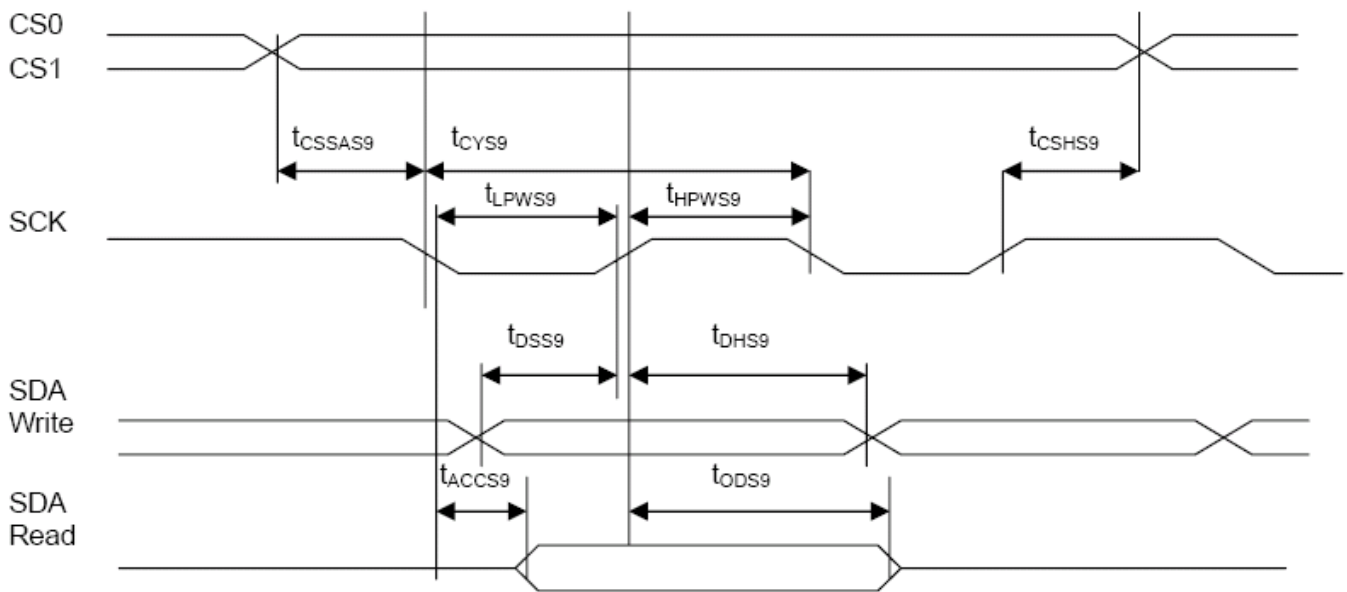


SERIAL INTERFACE TIMING CHARACTERISTICS (FOR S8)

($2.5V \leq V_{DD} < 3.6V$, $T_a = -30$ to $+85^\circ C$)

Symbol	Signal	Description	Condition	Min. (nS)	Max. (nS)
t_{ASS8}	CD	Address setup time		0	—
t_{AHS8}		Address hold time		0	—
t_{CYS8}	SCK	System cycle time		120 / 36	—
t_{LPWS8}		Low pulse width	(Read / Write)	60 / 18	—
t_{HPWS8}		High pulse width		60 / 18	—
t_{ACCS8}	SDA	Read access time	(Read)	—	50
t_{ODS8}		Output disable time		15	—
t_{DSS8}	SDA	Data setup time	(Write)	15	—
t_{DHS8}		Data hold time		0	—
t_{CSSAS8}	CS1/CS0	Chip select setup time	(Read / Write)	0 / 0	
t_{CSHS8}				0 / 0	

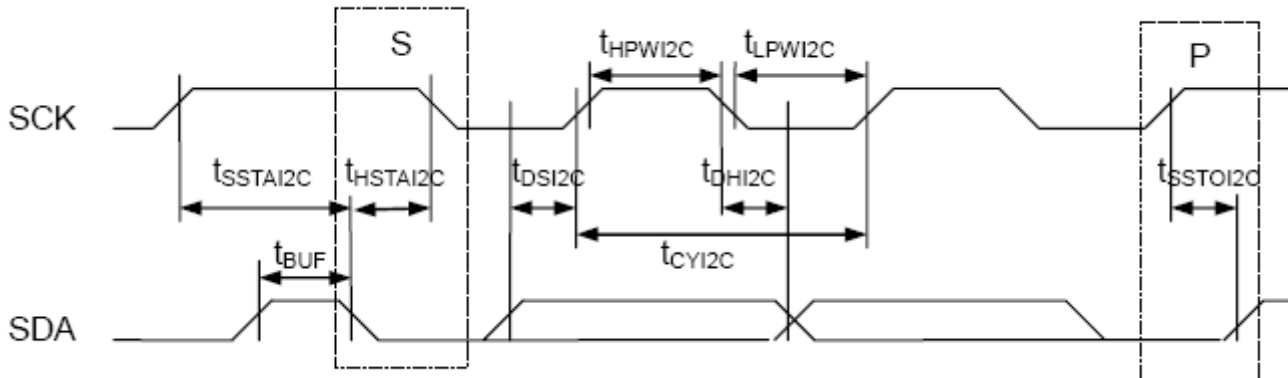
SERIAL INTERFACE TIMING DIAGRAM (FOR S8)



SERIAL INTERFACE TIMING CHARACTERISTICS (FOR S8)

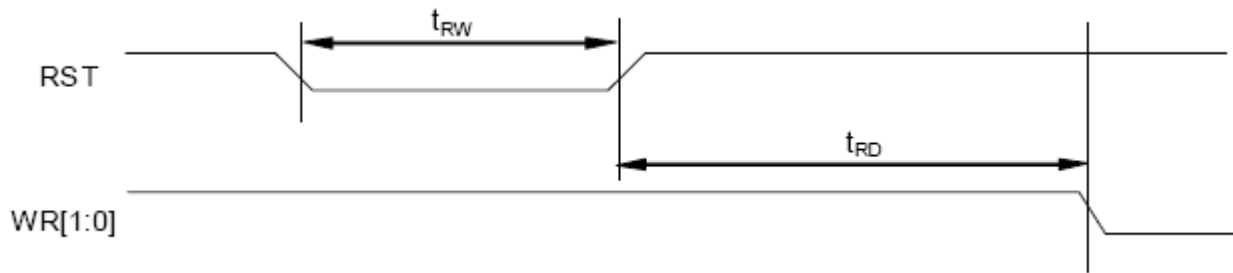
($2.5V \leq V_{DD} < 3.6V$, $T_a = -30$ to $+85^\circ C$)

Symbol	Signal	Description	Condition	Min. (nS)	Max. (nS)
t_{CYS9}		System cycle time		120 / 36	–
t_{LPWS9}	SCK	Low pulse width	(Read / Write)	60 / 18	–
t_{HPWS9}		High pulse width		60 / 18	–
t_{ACCS9}	SDA	Read access time	(Read)	–	50
t_{ODS9}		Output disable time		15	–
t_{DSS9}	SDA	Data setup time	(Write)	15	–
t_{DHS9}		Data hold time		0	–
t_{CSSAS9}	CS1/CS0	Chip select setup time	(Read / Write)	0 / 0	
t_{CSHS9}				0 / 0	

SERIAL INTERFACE TIMING DIAGRAM (FOR I²C)SERIAL INTERFACE TIMING CHARACTERISTICS (FOR I²C)

($2.5V \leq V_{DD} < 3.6V$, $T_a = -30$ to $+85^\circ C$)

Symbol	Signal	Description	Condition	Min. (nS)	Max. (nS)
t_{CVI2C}	SCK	SCK cycle time	$t_r + t_f \leq 100nS$ (Read / Write)	580 / 276	—
t_{LPWI2C}		Low pulse width		290 / 138	
t_{HPWI2C}		High pulse width		290 / 138	
t_{DSI2C}	SCK	Data setup time		33	—
t_{DHI2C}		Data hold time		11	
$t_{SSTAI2C}$		START Setup time		28	
$t_{HSTAI2C}$		START Hold time		50	
$t_{SSTOI2C}$		STOP setup time		28	
t_{BUF}	SDA	Bus Free time between STOP and START condition		165	—

RESET TIMING DIAGRAM**RESET TIMING**

($1.65V \leq V_{DD} < 3.6V$, $T_a = -30$ to $+85^\circ C$)

Symbol	Signal	Description	Condition	Min.	Max.
t_{RW}	RST	Reset low pulse width		3 μS	–
t_{RD}	RST, WR	Reset to WR pulse delay		10 mS	–

ELECTRO-OPTICAL CHARACTERISTICS

MEASURING CONDITION: POWER SUPPLY = $V_{OP} / 64 \text{ Hz}$
 TEMPERATURE = $23 \pm 5 \text{ }^\circ\text{C}$
 RELATIVE HUMIDITY = $60 \pm 20 \%$

ITEM	SYMBOL	UNIT	TYP. STN
RESPONSE TIME	Ton	ms	320
	Toff	ms	430
CONTRAST RATIO	Cr	-	8
VIEWING ANGLE (Cr \geq 2)	V3:00	$^\circ$	40
	V6:00	$^\circ$	55
	V9:00	$^\circ$	40
	V12:00	$^\circ$	35

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

RELIABILITY OF LCD MODULE

NO.	Item	Test Condition For normal temperature	Test Condition For wide temperature	Time
1	High temperature operating	50°C	70°C	240 hours
2	Low temperature operating	0°C	-20°C	240 hours
3	High temperature storage	60°C	80°C	240 hours
4	Low temperature storage	-10°C	-30°C	240 hours
5	Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
6	Temperature cycling	-10°C to 60°C 30 Min Dwell	-30°C to 80°C 30 Min Dwell	5 cycle
7	Vibration Test at LCM Level	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	Freq 10-55 Hz Sweep rate: 10-55-10 at 1 min Sweep mode Linear Displacement: 2 mm p-p 1 Hour each for X, Y, Z	—

Inspection condition:

No. 1 ~ 6:

The samples should be placed in room temperature for 2 hours before inspection.

Acceptance criteria:

No non-conformance found in functional and cosmetic.

SAMPLING METHOD

SAMPLING PLAN: MIL-STD 105E

CLASS OF AQL: LEVEL II/ SINGLE SAMPLING
 MAJOR-0.65% MINOR – 1.5%

QUALITY STANDARD

DEFECT	CRITERIA	TYPE	FIGURE
SHORT CIRCUIT	-	MAJOR	-
MISSING SEGMENT	-	MAJOR	-
UNEVEN / POOR CONTRAST	-	MAJOR	-
CROSS TALK	-	MAJOR	-
PIN HOLE	$MAX(a,b) \leq 1/4 W$	MINOR	1
EXCESS SEGMENT	$MAX(c,d) \leq 1/4 T$	MINOR	1
BUBBLES	$d^* \geq 0.2$ QTY=0	MINOR	2
BLACKS SPOTS	$d \leq 0.3$ N.A.** $0.3 < d \leq 0.4$ QTY \leq 1 $0.4 < d$ QTY=0	MINOR	2
LINE SCRATCHES	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3
BLACK LINE	$x \geq 0.7$ $y \geq 0.05$ QTY=0	MINOR	3

*d = MAX (d₁,d₂)

** N. A . = NOT APPLICABLE

DEFECT TABLE : B

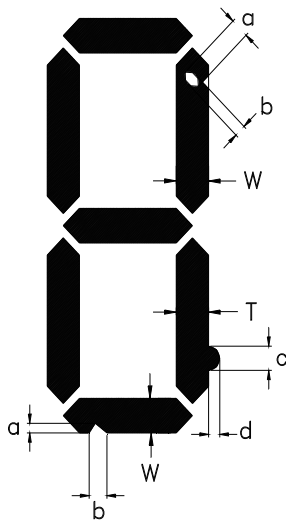
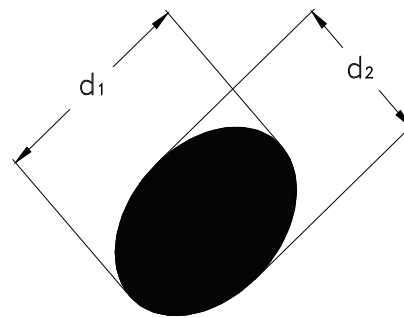
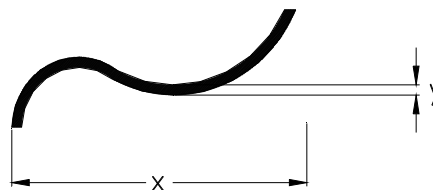


fig . 1



POLARIZER BUBBLES / SPOTS

fig . 2



LINE SCRATCHES / BLACK LINE

fig . 3

QUALITY STANDARD (CONT .)

DEFECT		CRITERIA	TYPE	FIGURE
CHIPS	CONTACT EDGE	$e \leq 1/2T$ $f \leq 1/3W$ $g \leq 3.5$	MINOR	4
	BOTTOM GLASS	$p \leq 1.0$ $q \leq 3.5$ $r \leq 1/2T$		4
	CORNER	$a \leq 1.5$ $b \leq W$		4
	TOP GLASS	$a \leq 3.0$ $b \leq 1/3T$ $c \leq 1/2W$		5
GLASS PROTRUSION		$a \leq 1/4 W$	MINOR	6
RAINBOW		-	MINOR	-

UNLESS STATE OTHERWISE , ALL UNIT ARE IN MILLIMETER .

DEFECT TABLE : B

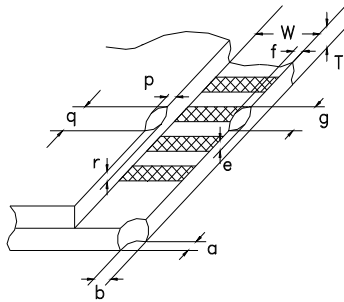


fig . 4

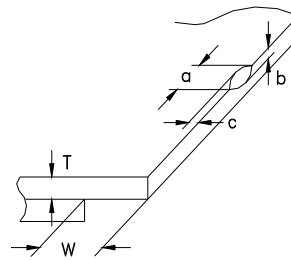


fig . 5

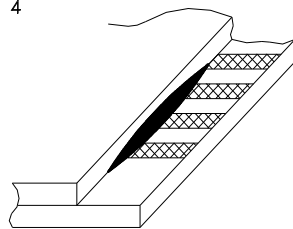
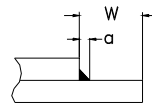


fig . 6



HANDLING PRECAUTIONS

(1) CAUTION OF LCD HANDLING & CLEANING

The polarizing plate on the surface of the panel is made from organic substances. Be very careful for chemicals not to touch the plate or it leads the polarizing plate to deteriorate.

If the use of a chemical is unavoidable, wipe the panel lightly with soft materials, such as gauze and absorbent cotton, soaked in a solvent.

*Usable solvent: Alcohol (ethanol, IPA and the like)

*Appropriate solvent: Ketones, ethyl alcohol

Avoid wiping with a dry cloth, since it could damage the surface of the polarizing plate and others.

(2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommended that any unused input terminal would be connected to V_{DD} or V_{SS} , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

(3) ESD PRECAUTION

Inputs and outputs are protected against electrostatic discharge in normal handling. However, to be totally safe, it is recommended to take normal precautions appropriate to handling LCM module. For example: product surface grounding. Always take ESD precaution when handling the *LCD Module*. Components are exposed for direct finger touches and can be damaged unless ESD precaution is taken.

(4) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed to direct sunshine or high temperature/humidity for long periods.

(5) CAUTION FOR OPERATION

The viewing angle can be adjusted by varying the LCD driving voltage V_O .

Driving voltage should be kept within specified range, excess voltage shortens display life.

Response time increases with decrease in temperature.

Display may turn black or dark Blue at temperature above its operational range; this is however not destructive and the display will return to normal once the temperature falls back to range.

Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured". They will recover once the display is turned off.

Condensation at terminals will cause malfunction and possible electrochemical reaction. Relative humidity of the environment should therefore be kept below 60%.

(6) SAFETY

Liquid crystal may leak out of a damaged LCD, it is recommended to wash off the liquid crystal by using solvents such as acetone or ethanol and should be burned up later.

If any liquid leak out of a damaged glass cell comes in contact with your hands, wash it off with soap and water immediately.

WARRANTY

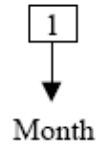
CLOVER will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Clover is limited to repair and/or replacement. Clover will not be responsible

APPENDIX

LOT INDICATION OF LCD MODULE

CODING SYSTEM:

1 DIGIT COLOR CODE:



COLOR CODE:

MONTH	COLOR	
1	BROWN	棕
2	RED	紅
3	ORANGE	橙
4	YELLOW	黃
5	GREEN	綠
6	BLUE	藍
7	PURPLE	紫
8	GREY	灰
9	WHITE	白
10	BLACK	黑
11	GOLD	金
12	SILVER	銀

3 TYPES OF LOCATION AS SHOWN BELOW:

